

10997 U.S. PTO

10/083161



02/27/02

## U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10083161	02/27/2002	711	111	2185	M. Kim
*APPLICANTS: Kimura Hiroaki; Shirahige Yuji; Yamazaki Iwao; 2186					
**CONTINUING DATA VERIFIED:					
**FOREIGN APPLICATIONS VERIFIED: JAPAN 2001-328022 10/25/2001					
PG-PUB DO NOT PUBLISH <input type="checkbox"/> RESCIND <input type="checkbox"/>					
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no				ATTORNEY DOCKET NO	
35 USC 118 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no				826.1799	
Verified and Acknowledged Examiner's Initials					
Title: Onchip control device and method					
U.S. DEPT. OF COMM. / PAT. & TM. PTO. 625L (Rev. 12-01)					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner			
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. Print Fig.
		Primary Examiner	
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner	
		PREPARED FOR ISSUE	
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